



Manufacturer: NI

Board Assembly Part Numbers

Part Number and Revision	Description
149021D-03L or later	PXIE-5831 MMWAVE VST - IF
149021D-04L or later	PXIE-5831 MMWAVE VST - ONE HEAD (NO SWITCH)
149021D-06L or later	PXIE-5831 MMWAVE VST - TWO HEADS (NO SWITCH, NO SWITCH)
149021D-08L or later	PXIE-5831 MMWAVE VST - ONE HEAD (ONE SWITCH)
149021D-05L or later	PXIE-5831 MMWAVE VST - ONE HEAD (TWO SWITCHES)
149021D-07L or later	PXIE-5831 MMWAVE VST - TWO HEADS (NO SWITCH, TWO SWITCHES)
149021D-211L or later	PXIE-5831 MMWAVE VST - TWO HEADS (ONE SWITCH, ONE SWITCH)
149021D-222L or later	PXIE-5831 MMWAVE VST - TWO HEADS (TWO SWITCHES, TWO SWITCHES)

Volatile and Non-Volatile Memory of Component Models

This device is composed of independent hardware models. Refer to the Letter of Volatility for each individual model listed below by going to ni.com/info and typing in the appropriate Info Code.

Model and Description	Info Code
MODULE ASSEMBLY, PXIE-3622	jevgxu
MODULE ASSEMBLY, PXIE-5820 BASEBAND VST	ljm9pw
MODULE ASSEMBLY, PXIE-5653 (NEL OCXO)	exbrpd
MODULE ASSEMBLY, MMRH-5582	<individual devices below>
mmRH-5581, 22-44 GHz mmWave Transceiver	t51x25
mmSW-2795, 23-45 GHz SP8T Switch	rblqpz



Terms and Definitions

Cycle Power:

The process of completely removing power from the device and its components and allowing for adequate discharge. This process includes a complete shutdown of the PC and/or chassis containing the device; a reboot is not sufficient for the completion of this process.

Volatile Memory:

Requires power to maintain the stored information. When power is removed from this memory, its contents are lost. This type of memory typically contains application specific data such as capture waveforms.

Non-Volatile Memory:

Power is not required to maintain the stored information. Device retains its contents when power is removed. This type of memory typically contains information necessary to boot, configure, or calibrate the product or may include device power up states.

User Accessible:

The component is read and/or write addressable such that a user can store arbitrary information to the component from the host using a publicly distributed NI tool, such as a Driver API, the System Configuration API, or MAX.

System Accessible:

The component is read and/or write addressable from the host without the need to physically alter the product.

Clearing:

Per *NIST Special Publication 800-88 Revision 1*, “clearing” is a logical technique to sanitize data in all User Accessible storage locations for protection against simple non-invasive data recovery techniques using the same interface available to the user; typically applied through the standard read and write commands to the storage device.

Sanitization:

Per *NIST Special Publication 800-88 Revision 1*, “sanitization” is a process to render access to “Target Data” on the media infeasible for a given level of effort. In this document, clearing is the degree of sanitization described.