**Board Assembly Part Number(s)**

| Part Number | Description |
|---|---|
| 184836K-01L or later | NI PXI-6508 |

**Manufacturer:** National Instruments

## Volatile Memory

| Type[1] | Size | User Accessible/ System Accessible[2] | Battery Backup? | Purpose | Method of Clearing[3] |
|---|---|---|---|---|---|
| *NONE* | | | | | |

## Non-Volatile Memory

| Type | Size | User Accessible/ System Accessible | Battery Backup? | Purpose | Method of Clearing |
|---|---|---|---|---|---|
| *EEPROM* | *8k x 8 bits* | *No/No* | *No* | *Device Configuration* | *None available to user* |

## Media Storage

| Type | Size | User Accessible/ System Accessible | Battery Backup? | Purpose | Method of Clearing |
|---|---|---|---|---|---|
| *NONE* | | | | | |

## Clearing Notes:

**EEPROM:** Not write protected, but there is no mechanism provided for writing to the EEPROM through the user API. No user data is stored in the EEPROM.

[1] Calibration constants that are stored in device EEPROMs include information for the device's full operating range. Calibration constants do not maintain any unique data for specific configurations at which the device is used unless otherwise specified.

[2] Items are designated **No** for the following reason(s):
a) Hardware changes or a unique software tool from National Instruments are required to modify contents of the memory listed.
b) Hardware-modifying software tools are not distributed to customers for any personal access or customization, also known as non-normal use.

[3] The designation *None Available to User* indicates that the ability to clear this memory is not available to the user under normal operation. The utilities required to clear the memory are not distributed by National Instruments to customers for normal use.



Contact: 866-275-6964
support@ni.com

**Terms and Definitions**

**User Accessible** Allows the user to directly write or modify the contents of the memory during normal instrument operation.

**System Accessible** Does not allow the user to access or modify the memory during normal instrument operation. However, system accessible memory may be accessed or modified by background processes. This can be something that is not deliberate by the user and can be a background driver implementation, such as storing application information in RAM to increase speed of use.

**Cycle Power** The process of completely removing power from the device and its components. This process includes a complete shutdown of the PC and/or chassis containing the device; a reboot is not sufficient for the completion of this process.

**Volatile Memory** Requires power to maintain the stored information. When power is removed from this memory, its contents are lost.

**Non-Volatile** Retains its contents when power is removed. This type of memory typically contains calibration or chip configuration information, such as power up states.

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