GETTING STARTED GUIDE
PXLe-5774
12-Bit, 6.4 GS/s, DC-Coupled, 2-Channel PXI FlexRIO Digitizer

This document explains how to install, configure, test, and use the PXLe-5774. You can program the PXLe-5774 with the following software options.

- FlexRIO driver software
- NI LabVIEW Instrument Design Libraries for FlexRIO (instrument design libraries)

**Note**  Before you begin, install and configure your chassis and controller.

**Note**  Adapter modules are not installable or interchangeable on PXLe-5774 devices.

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<th>Description</th>
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<td>FlexRIO Help</td>
<td>Available at <a href="http://ni.com/manuals">ni.com/manuals</a></td>
<td>Contains information about the FPGA module front panel connectors and I/O, programming instructions, and I/O component-level IP (CLIP).</td>
</tr>
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<td>LabVIEW Examples</td>
<td>Available in NI Example Finder. In LabVIEW, click Help»Find Examples»Hardware Input and Output»FlexRIO.</td>
<td>Contains examples of how to run FPGA VIs and Host VIs on your device.</td>
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### Verifying the System Requirements

To use the PXIe-5774, your system must meet certain requirements. For more information about minimum system requirements, recommended system, and supported application development environments (ADEs), refer to the readme, which is available on the software media or online at [ni.com/updates](http://ni.com/updates).
Unpacking the Kit

⚠️ **Notice** To prevent electrostatic discharge (ESD) from damaging the device, ground yourself using a grounding strap or by holding a grounded object, such as your computer chassis.

1. Touch the antistatic package to a metal part of the computer chassis.
2. Remove the device from the package and inspect the device for loose components or any other sign of damage.

⚠️ **Notice** Never touch the exposed pins of connectors.

>Note> **Note** Do not install a device if it appears damaged in any way.

3. Unpack any other items and documentation from the kit.

Store the device in the antistatic package when the device is not in use.

**PXIe-5774 Kit Contents**

The following items are included in the device kit:

- PXIe-5774
- Documentation:
  - Maintain Forced-Air Cooling Note to Users
  - PXIe-5774 Getting Started Guide (this document)
  - PXIe-5774 Safety, Environmental, and Regulatory Information
Preparing the Environment

Ensure the environment in which you are using the PXIe-5774 meets the following specifications.

Operating environment

<table>
<thead>
<tr>
<th>Specification</th>
<th>Requirement</th>
</tr>
</thead>
<tbody>
<tr>
<td>Ambient temperature range</td>
<td>0 °C to 55 °C</td>
</tr>
<tr>
<td>Relative humidity range</td>
<td>10% to 90%, noncondensing</td>
</tr>
<tr>
<td>Maximum altitude</td>
<td>2,000 m (800 mbar) (at 25 °C ambient temperature)</td>
</tr>
<tr>
<td>Pollution Degree</td>
<td>2</td>
</tr>
</tbody>
</table>

Indoor use only.

Note For complete specifications, refer to the specifications document for your device at ni.com/manuals.

Installing the Software and Driver

Before installing your hardware, you must install the application software and instrument driver. Visit Ni FlexRIO Driver Supported Versions for FlexRIO Adapters and Modules to determine which minimum software versions you need for your device. Install the software in the following order:

1. Install LabVIEW.

   Refer to the LabVIEW Installation Guide for installation instructions for LabVIEW and system requirements for the LabVIEW software. Refer to the LabVIEW Upgrade Notes for additional information about upgrading to the most recent version of LabVIEW for Windows. Documentation for LabVIEW is available at ni.com/manuals.

2. Install the LabVIEW FPGA Module.

   Refer to the LabVIEW FPGA Module Release and Upgrade Notes for installation instructions and information about getting started with the LabVIEW FPGA Module. Documentation for the LabVIEW FPGA Module is available at ni.com/manuals.

3. (Optional) Install the LabVIEW Real-Time Module.

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1 The PXIe-5774 requires a chassis with slot cooling capacity ≥58 W. Not all chassis with slot cooling capacity ≥58 W can achieve this ambient temperature range. Refer to the PXI Chassis Manual for specifications to determine the ambient temperature ranges your chassis can achieve.
Refer to the LabVIEW Real-Time Module Release and Upgrade Notes for system requirements, installation instructions, and additional information about using the LabVIEW Real-Time Module.

4. Install FlexRIO.

Refer to the FlexRIO Readme for system requirements and installation instructions for FlexRIO. Documentation for FlexRIO is available at ni.com/manuals.

Installing the PXIe-5774

⚠️ Notice  To prevent damage to the PXIe-5774 caused by ESD or contamination, handle the module using the edges or the metal bracket.

1. Ensure the AC power source is connected to the chassis before installing the module.

   The AC power cord grounds the chassis and protects it from electrical damage while you install the module.

2. Power off the chassis.

3. Inspect the slot pins on the chassis backplane for any bends or damage prior to installation. Do not install a module if the backplane is damaged.

4. Remove the black plastic covers from all the captive screws on the module front panel.

5. Identify a supported slot in the chassis. The following figure shows the symbols that indicate the slot types.

   **Figure 1. Chassis Compatibility Symbols**

   ![Figure 1. Chassis Compatibility Symbols](image)

   1. PXI Express System Controller Slot
   2. PXI Peripheral Slot
   3. PXI Express Hybrid Peripheral Slot
   4. PXI Express System Timing Slot
   5. PXI Express Peripheral Slot

   PXIe-5774 modules can be placed in PXI Express peripheral slots, PXI Express hybrid peripheral slots, or PXI Express system timing slots.

6. Touch any metal part of the chassis to discharge static electricity.

7. Ensure that the ejector handle is in the downward (unlatched) position.

8. Place the module edges into the module guides at the top and bottom of the chassis. Slide the module into the slot until it is fully inserted.
Figure 2. Module Installation

1. Chassis
2. Hardware Module
3. Ejector Handle in Downward (Unlatched) Position

9. Latch the module in place by pulling up on the ejector handle.
10. Secure the module front panel to the chassis using the front-panel mounting screws.

   **Note** Tightening the top and bottom mounting screws increases mechanical stability and also electrically connects the front panel to the chassis, which can improve the signal quality and electromagnetic performance.

11. Cover all empty slots using EMC filler panels or fill using slot blockers to maximize cooling air flow, depending on your application.
12. Power on the chassis.

**Installing the Ferrite on the DIO Cable**

**Notice** To ensure the specified EMC performance, install the snap-on ferrite bead (National Instruments part number 781233-02) in accordance with these instructions.

1. Open the ferrite bead and place the cable inside the center of the bead. Ensure the ferrite bead is as close to where the cable connects to the PXIe-5774 as practical.
2. Close the ferrite bead until the locking tabs engage securely.
**Figure 3. Snap-On Ferrite Bead Installation**

1. Molex™ Nano-Pitch I/O™ connector
2. Ferrite

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**PXle-5774 Front Panel and Pinout**

**PXle-5774 Front Panel**

The following figure shows the PXle-5774 front panel.
The following table describes the signal connections for the PXIe-5774.

<table>
<thead>
<tr>
<th>Connector</th>
<th>Description</th>
<th>Function</th>
</tr>
</thead>
<tbody>
<tr>
<td>DIO</td>
<td>Molex Nano-Pitch DIO connector</td>
<td>Multi-signal DIO connector that provides access to FPGA multi-gigabit transceivers (MGTs) and general-purpose LVCMOS signals.</td>
</tr>
<tr>
<td>AI 0</td>
<td>Standard SMA female connector</td>
<td>Analog input connection.</td>
</tr>
<tr>
<td>AI 1</td>
<td></td>
<td></td>
</tr>
</tbody>
</table>
### Connector Description

<table>
<thead>
<tr>
<th>Connector</th>
<th>Description</th>
<th>Function</th>
</tr>
</thead>
<tbody>
<tr>
<td>REF/CLK IN</td>
<td>Standard SMA female connector</td>
<td>Input for an external Reference Clock or Sample Clock.</td>
</tr>
<tr>
<td>TRIG IN</td>
<td>Standard SMA female connector</td>
<td>Analog IN trigger.</td>
</tr>
<tr>
<td>TRIG OUT</td>
<td>Standard SMA female connector</td>
<td>Digital OUT trigger.</td>
</tr>
</tbody>
</table>

### Digital I/O Pinout

The following figure shows the Digital I/O (DIO) connector pinout.

![Figure 5. Digital I/O Connector](image)

The following table lists the available pins on the DIO connector.

<table>
<thead>
<tr>
<th>Signal</th>
<th>Type</th>
<th>Direction</th>
</tr>
</thead>
<tbody>
<tr>
<td>MGT Tx± &lt;0..3&gt;</td>
<td>Xilinx UltraScale GTH</td>
<td>Output</td>
</tr>
<tr>
<td>MGT Rx± &lt;0..3&gt;</td>
<td>Xilinx UltraScale GTH</td>
<td>Input</td>
</tr>
<tr>
<td>Signal</td>
<td>Type</td>
<td>Direction</td>
</tr>
<tr>
<td>--------</td>
<td>------------</td>
<td>-----------</td>
</tr>
<tr>
<td>DIO &lt;0..7&gt;</td>
<td>Single-ended</td>
<td>Bidirectional</td>
</tr>
<tr>
<td>5.0 V</td>
<td>DC</td>
<td>Output</td>
</tr>
<tr>
<td>GND</td>
<td>Ground</td>
<td>—</td>
</tr>
</tbody>
</table>

⚠️ Notice ⚠️ The maximum input signal levels are valid only when the module is powered on. To avoid permanent damage to the PXIe-5774, do not apply a signal to the device when the module is powered down.

⚠️ Notice ⚠️ Connections that exceed any of the maximum ratings of any connector on the PXIe-5774 can damage the device and the system. NI is not liable for any damage resulting from such connections.

### Configuring the PXIe-5774 in MAX

Use Measurement & Automation Explorer (MAX) to configure your NI hardware. MAX informs other programs about which NI hardware products are in the system and how they are configured. MAX is automatically installed with FlexRIO.

1. Launch MAX.
2. In the configuration tree, expand Devices and Interfaces to see the list of installed NI hardware.

   Installed modules appear under the name of their associated chassis.
3. Expand your Chassis tree item.

   MAX lists all modules installed in the chassis. Your default names may vary.

   ❗️ Note ❗️ If you do not see your module listed, press <F5> to refresh the list of installed modules. If the module is still not listed, power off the system, ensure the module is correctly installed, and restart.
4. Record the identifier MAX assigns to the hardware. Use this identifier when programming the PXIe-5774.
5. Self-test the hardware by selecting the item in the configuration tree and clicking Self-Test in the MAX toolbar.

   The MAX self-test performs a basic verification of hardware resources.

### FlexRIO Examples

FlexRIO includes several example applications for LabVIEW. These examples serve as interactive tools, programming models, and as building blocks in your own applications.
Accessing FlexRIO Examples

FlexRIO examples are available in LabVIEW's NI Example Finder. Complete the following steps to access the examples by task.

1. In LabVIEW, click Help » Find Examples.
2. In the NI Example Finder window that appears, click Hardware Input and Output » FlexRIO.

The examples are sorted by task. Click on an example and refer to the Information window for a description of the example. Refer the Requirements window for a list of hardware devices that can run the example.

You can also click the Search tab to search all installed examples by keyword. For example, search for FlexRIO to locate all FlexRIO examples.

Examples also are available online that demonstrate FlexRIO basics, such as using DRAM, acquiring data from adapter modules, and performing high throughput streaming. Refer to ni.com/examples for these examples and for more information.

Block Diagram

The following figure shows a block diagram of the carrier portion of the PXIe-5774.

Figure 6. Carrier Block Diagram (KU040 and KU060)
The following figure shows a block diagram of the I/O portion of the PXIe-5774.

**Figure 7. PXIe-5774 Block Diagram**

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Component-Level Intellectual Property (CLIP)

The LabVIEW FPGA Module includes component-level intellectual property (CLIP) for HDL IP integration. FlexRIO devices support two types of CLIP: user-defined and socketed.

- **User-defined CLIP** allows you to insert HDL IP into an FPGA target, enabling VHDL code to communicate directly with an FPGA VI.
- **Socketed CLIP** provides the same IP integration of the user-defined CLIP, but it also allows the CLIP to communicate directly with circuitry external to the FPGA. Adapter module socketed CLIP allows your IP to communicate directly with both the FPGA VI and the external adapter module connector interface.

The PXIe-5774 ships with socketed CLIP items that add module I/O to the LabVIEW project.
Making a Measurement

Making a Measurement with LabVIEW
1. Launch LabVIEW.
2. Select Help»Find Example.
3. Open the example VI that you want to use by selecting Hardware Input and Output» FlexRIO.
4. Follow any setup, configuration, and execution instructions in the VI.

Troubleshooting
If an issue persists after you complete a troubleshooting procedure, contact NI technical support or visit ni.com/support.

What Should I Do if the PXIe-5774 Doesn't Appear in MAX?
1. In the MAX configuration tree, expand Devices and Interfaces.
2. Expand the Chassis tree to see the list of installed hardware, and press <F5> to refresh the list.
3. If the module is still not listed, power off the system, ensure that all hardware is correctly installed, and restart the system.
4. Navigate to the Device Manager.

<table>
<thead>
<tr>
<th>Operating System</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>Windows 10/8.1</td>
<td>Right-click the Start button, and select Device Manager.</td>
</tr>
<tr>
<td>Windows 7</td>
<td>Select Start»Control Panel»Device Manager.</td>
</tr>
</tbody>
</table>

5. Verify the PXIe-5774 appears in the Device Manager.
   a) Under an NI entry, confirm that a PXIe-5774 entry appears.

   ![Note] If you are using a PC with a device for PXI remote control system, under System Devices, also confirm that no error conditions appear for the PCI-to-PCI Bridge.

   b) If error conditions appear, reinstall FlexRIO and the PXIe-5774.

What Should I Do if the PXIe-5774 Fails the Self-Test?
1. Restart the system.
2. Launch MAX, and perform the self-test again.
3. Power off the chassis.
4. Reinstall the failed module in a different slot.
5. Power on the chassis.
6. Perform the self-test again.

Where to Go Next

Refer to the following figure for information about other product tasks and associated resources for those tasks.

Located online at ni.com/manuals

EXPLORE
the application development environment (ADE) for your application.

LEARN
about hardware features or review device specifications.

CREATE
custom applications with an application programming interface (API).

DISCOVER
more about your products through ni.com.

Located using the NI Example Finder

EXPLORE

LEARN

CREATE

Worldwide Support and Services

The NI website is your complete resource for technical support. At ni.com/support, you have access to everything from troubleshooting and application development self-help resources to email and phone assistance from NI Application Engineers.

Visit ni.com/services for information about the services NI offers.

Visit ni.com/register to register your NI product. Product registration facilitates technical support and ensures that you receive important information updates from NI.

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support outside the United States, visit the *Worldwide Offices* section of *ni.com/niglobal* to access the branch office websites, which provide up-to-date contact information.