

SPECIFICATIONS

PXIe-5433

80 MHz Bandwidth, 16-Bit PXI Waveform Generator

These specifications apply to the one-channel and two-channel PXIe-5433.

Contents

| | |
|------------------------------------|----|
| Definitions..... | 1 |
| Conditions..... | 2 |
| Analog Output..... | 2 |
| Standard Function..... | 4 |
| Arbitrary Waveform..... | 11 |
| General..... | 13 |
| Clock..... | 15 |
| PFI I/O..... | 16 |
| Trigger..... | 16 |
| Marker..... | 16 |
| Calibration..... | 17 |
| Environment..... | 17 |
| Operating Environment..... | 18 |
| Storage Environment..... | 18 |
| Shock and Vibration..... | 18 |
| Physical..... | 18 |
| Compliance and Certifications..... | 19 |
| Safety..... | 19 |
| Electromagnetic Compatibility..... | 19 |
| CE Compliance..... | 19 |
| Online Product Certification..... | 20 |
| Environmental Management..... | 20 |

Definitions

Warranted specifications describe the performance of a model under stated operating conditions and are covered by the model warranty.

The following characteristic specifications describe values that are relevant to the use of the model under stated operating conditions but are not covered by the model warranty.

- *Typical* specifications describe the performance met by a majority of models.
- *Nominal* specifications describe an attribute that is based on design, conformance testing, or supplemental testing.
- *Measured* specifications describe the measured performance of a representative model.

Specifications are *Nominal* unless otherwise noted.

Conditions

All specifications are valid under the following conditions unless otherwise noted:

- Signals terminated with 50 Ω to ground
- Load impedance set to 50 Ω
- Amplitude set to 2.4 V_{pk-pk}
- **Analog Path** property or NIFGEN_ATTR_ANALOG_PATH attribute set to **Main** (default)
- Reference clock set to **Onboard Reference Clock**

Warranted and typical specifications are valid under the following conditions unless otherwise noted:

- Ambient temperature range of 0 °C to 55 °C
- 15-minute warm-up time before operation
- Self-calibration performed after instrument is stable
- External calibration cycle maintained and valid
- PXI Express chassis fan speed set to HIGH, foam fan filters removed if present, and empty slots contain PXI chassis slot blockers and filler panels

Analog Output

| | |
|---------------------------------|-------------------------|
| Number of channels ¹ | 1 or 2 |
| Output type | Referenced single-ended |
| Connector type | SMA |
| DAC resolution | 16 bits |

¹ Channels support independent waveform generation.

Amplitude range², in 0.16 dB steps

| | |
|--|---|
| 50 Ω load | 0.00775 V_{pk-pk} to 12 V_{pk-pk} |
| Open load | 0.0155 V_{pk-pk} to 24 V_{pk-pk} |
| Offset range | $\pm 50\%$ of <i>Amplitude Range</i> (V_{pk-pk}) ³ |
| Offset resolution | 16-bit full-scale range |
| DC accuracy ⁴ | |
| Within ± 5 °C of self-calibration temperature | $\pm 0.35\%$ of <i>Amplitude Range</i> $\pm 0.35\%$ of <i>Offset Requested</i> ± 500 μV , warranted ⁵ |
| 0 °C to 55 °C | $\pm 0.55\%$ of <i>Amplitude Range</i> $\pm 0.55\%$ of <i>Offset Requested</i> ± 500 μV , typical |
| AC amplitude accuracy ⁶ (within ± 5 °C of self-calibration temperature) | $\pm 1.0\% \pm 1$ m V_{pk-pk} , warranted |
| Output impedance | 50 Ω |
| Load impedance | Output waveform is compensated for user-specified impedances |
| Output coupling (ground referenced) | DC |
| Output enable ⁷ | Software-selectable |
| Maximum output overload ⁸ | ± 12 V_{pk-pk} from a 50 Ω source |
| Waveform summing | Supported ⁹ |

² Amplitude values assume the full scale of the DAC is utilized. NI-FGEN uses waveforms less than the full scale of the DAC to create amplitudes smaller than the minimum value.

³ For example, a 5.5 V_{pk-pk} range equals ± 2.75 V maximum offset. Offset range has a limitation of ± 12 V absolute signal swing into high-impedance loads ($Amplitude + |Offset| \leq 12$ V into high-impedance load or 6 V into 50 Ω load).

⁴ Terminated with high-impedance load (load impedance set to 1 M Ω). The analog path is calibrated for amplitude, gain, and offset errors.

⁵ Where *Amplitude Range* is the requested amplitude in V_{pk-pk} . For example, a DC signal with an amplitude range of 16 V_{pk-pk} and offset of 1.5 will calculate DC accuracy using the following equation: $\pm[(0.35\% * 16 V) + (0.35\% * 1.5 V) + 500 \mu V] = \pm 61.75$ mV. The DC standard function always uses the 24 V_{pk-pk} amplitude range.

⁶ With 50 kHz sine wave and terminated with high-impedance load.

⁷ When the output path is disabled, the channel output is terminated to ground with a 50 Ω , 1 W resistor.

⁸ No damage occurs if the analog output channels are shorted to ground indefinitely.

⁹ The output terminals of multiple PXIe-5433 waveform generators can be connected together.

Standard Function

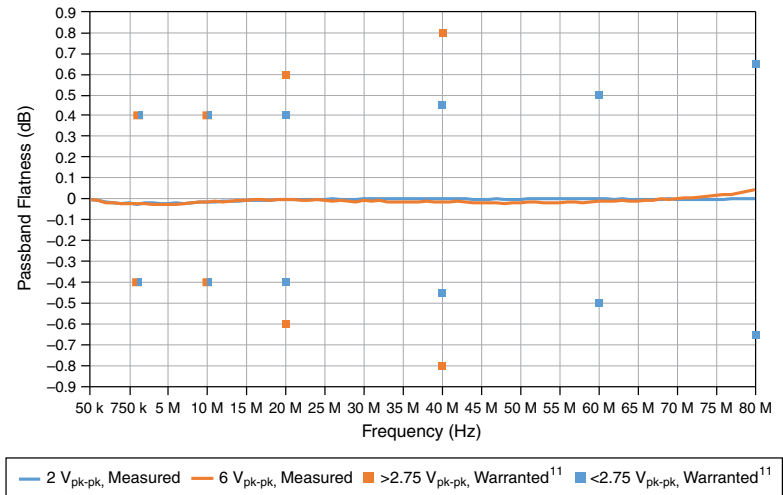
Sine Wave

| | |
|---------------------|-----------------|
| Frequency range | 0 MHz to 80 MHz |
| Frequency step size | 2.84 μ Hz |

Table 1. Passband Flatness¹⁰, Warranted

| Sine Frequency | Passband Flatness (dB), Warranted | |
|----------------------|--------------------------------------|-------------------|
| | 0.06 V_{pk-pk} to 2.75 V_{pk-pk} | >2.75 V_{pk-pk} |
| 1 MHz | ± 0.4 | ± 0.4 |
| 10 MHz | ± 0.4 | ± 0.4 |
| 20 MHz | ± 0.4 | ± 0.6 |
| 40 MHz ¹¹ | ± 0.45 | ± 0.8 |
| 60 MHz ¹¹ | ± 0.5 | — |
| 80 MHz ¹¹ | ± 0.65 | — |

Figure 1. Passband Flatness



¹⁰ Normalized to 50 kHz.

¹¹ With sine frequencies 40 MHz or higher and ambient temperatures above 45 °C, add ± 0.015 dB/°C to the passband flatness specification.

Table 2. Spurious-Free Dynamic Range (SFDR) with Harmonics¹², Measured

| Sine Frequency | SFDR with Harmonics (dBc), Measured | | |
|----------------|-------------------------------------|-----------------------------------|---------------------------------|
| | 0.1 V_{pk-pk} to 1 V_{pk-pk} | 1 V_{pk-pk} to 2.75 V_{pk-pk} | >2.75 V_{pk-pk} ¹³ |
| 1 MHz | 62 | 84 | 77 |
| 3 MHz | 62 | 82 | 63 |
| 5 MHz | 61 | 82 | 58 |
| 10 MHz | 61 | 77 | 52 |
| 20 MHz | 61 | 72 | 44 |
| 30 MHz | 61 | 68 | 40 |
| 40 MHz | 58 | 63 | 35 |
| 80 MHz | 44 | 47 | — |

Table 3. Spurious-Free Dynamic Range (SFDR) without Harmonics¹², Measured

| Sine Frequency | SFDR without Harmonics (dBc), Measured | | |
|----------------|--|-----------------------------------|---------------------------------|
| | 0.1 V_{pk-pk} to 1 V_{pk-pk} | 1 V_{pk-pk} to 2.75 V_{pk-pk} | >2.75 V_{pk-pk} ¹³ |
| 1 MHz | 62 | 84 | 92 |
| 3 MHz | 62 | 84 | 92 |
| 5 MHz | 62 | 84 | 92 |
| 10 MHz | 61 | 83 | 90 |
| 20 MHz | 61 | 83 | 90 |
| 30 MHz | 61 | 83 | 83 |
| 40 MHz | 61 | 83 | 83 |
| 80 MHz | 61 | 83 | — |

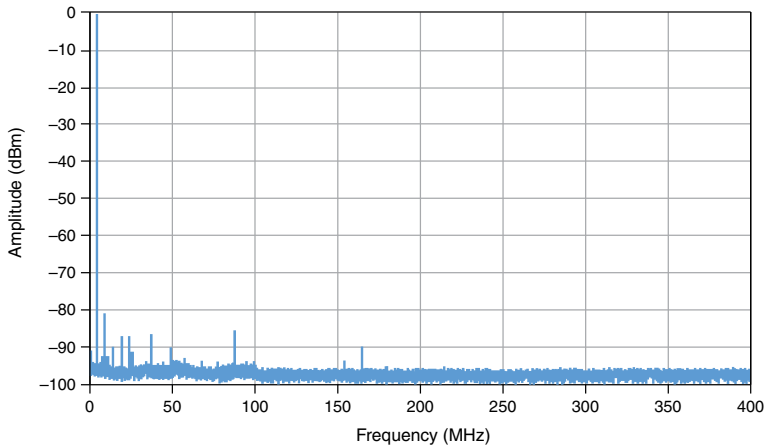
¹² At amplitude of -1 dBFS, measured from DC to 400 MHz, and limited to a -90 dBm spur at low amplitudes.

¹³ Full-scale amplitude follows operation curve in [Figure 11](#), on page 14.

Table 4. Total Harmonic Distortion (THD)¹⁴, Measured

| Sine Frequency | THD (dBc), Measured | |
|----------------|---|--|
| | 0.1 V _{pk-pk} to 2.75 V _{pk-pk} | 2.75 V _{pk-pk} to 12 V _{pk-pk} ¹³ |
| 1 MHz | 87 | 76 |
| 3 MHz | 81 | 62 |
| 5 MHz | 80 | 56 |
| 10 MHz | 71 | 49 |
| 20 MHz | 68 | 43 |
| 30 MHz | 68 | 39 |
| 40 MHz | 64 | 35 |
| 80 MHz | 47 | — |

Figure 2. 5 MHz Spectrum¹⁵ at 0.6 V_{pk-pk}, Measured



¹⁴ At amplitude of -1 dBFS and measured from DC to the sixth harmonic.

¹⁵ Noise floor is limited by the noise floor of the measurement device.

Figure 3. 10 MHz Spectrum¹⁵ at 2 V_{pk-pk}, Measured

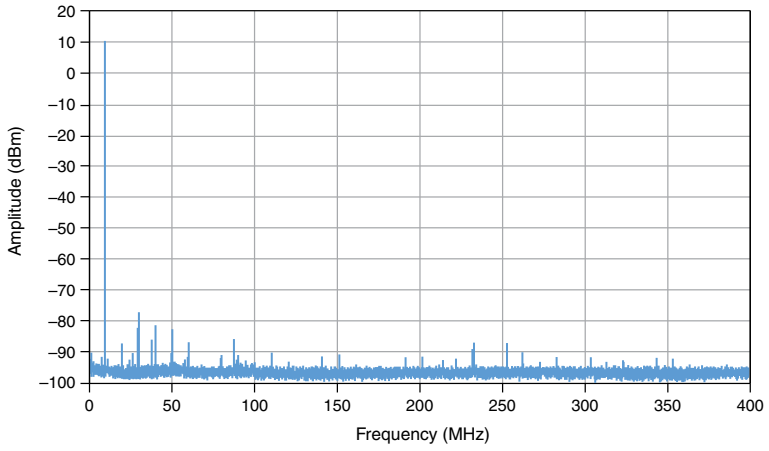


Figure 4. 1 MHz Spectrum¹⁵ at 6.5 V_{pk-pk}, Measured

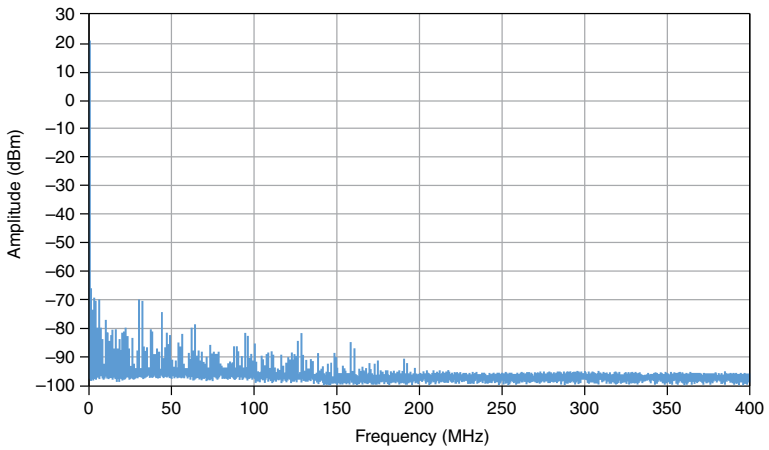


Table 5. Average Noise Density¹⁶, Typical

| Amplitude | Average Noise Density, Typical | |
|-------------------------|--------------------------------|------------------------|
| | dBm/Hz | $\frac{nV}{\sqrt{Hz}}$ |
| 0.06 V _{pk-pk} | -154 | 3.9 |
| 0.1 V _{pk-pk} | -154 | 3.9 |
| 0.4 V _{pk-pk} | -150 | 5.8 |
| 1 V _{pk-pk} | -145 | 13 |
| 2 V _{pk-pk} | -141 | 20 |
| 4 V _{pk-pk} | -132 | 53 |
| 12 V _{pk-pk} | -125 | 107 |

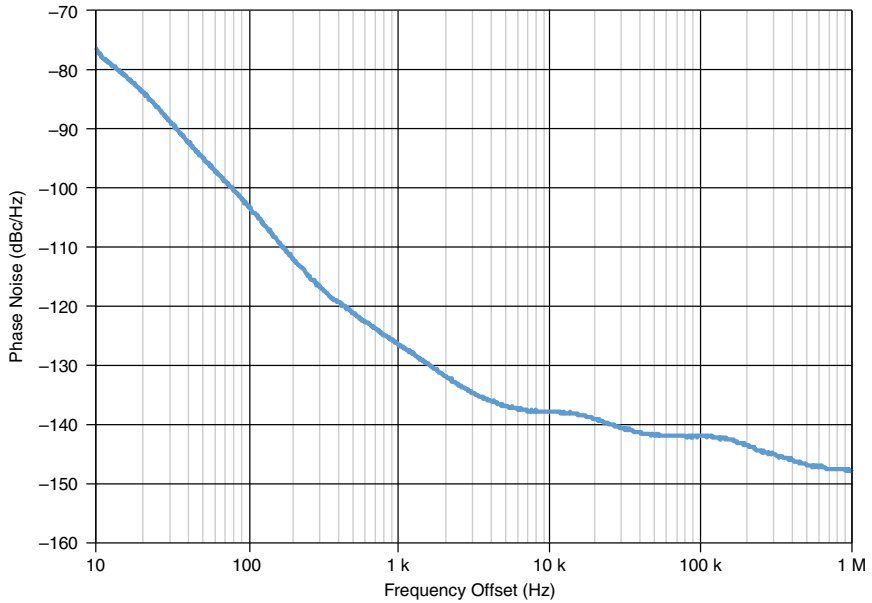
Channel-to-channel skew¹⁷

| | |
|--------------------------|---------|
| <2.75 V _{pk-pk} | ±110 ps |
| >2.75 V _{pk-pk} | ±275 ps |

¹⁶ At small amplitudes, average noise density is limited by a -154 dBm/Hz noise floor.

¹⁷ With a 20 MHz sine wave and both channels configured to the same amplitude.

Figure 5. Phase Noise¹⁸, Measured



Jitter (RMS)¹⁹ 207 fs

Square Waveforms

Frequency range

2.75 V_{pk-pk} 0 MHz to 50 MHz

12 V_{pk-pk} 0 MHz to 30 MHz

Frequency step size 2.84 μHz

Minimum on/off time²⁰ 8.25 ns

Duty cycle resolution <0.001%

Rise/fall time²¹

<2.75 V_{pk-pk} 4.5 ns, measured

>2.75 V_{pk-pk}²² 5.4 ns, measured

¹⁸ With 80 MHz carrier and locked to the internal timebase with spurs removed.

¹⁹ With 80 MHz carrier, integrated from 100 Hz to 100 kHz, and locked to the internal timebase.

²⁰ Used for calculating duty cycle limit: $Minimum\ Duty\ Cycle = (100\% * Minimum\ On\ Time) \div T_{period}$ and $Maximum\ Duty\ Cycle = 100\% - Minimum\ Duty\ Cycle$. For more information about the relationship between minimum on/off time and duty cycle specifications, refer to ni.com.

²¹ Rise time measured from 10% to 90%.

²² Rise time will vary with amplitude due to operational amplifier slew rate saturation.

Aberration

| | |
|----------------------------|------------------|
| $<2.75 V_{pk-pk}$ | 1.0%, measured |
| $>2.75 V_{pk-pk}$ | 5.0%, measured |
| Jitter (RMS) ²³ | 1.5 ps, measured |

Figure 6. Square Waveform Step Response at 2.75 V_{pk-pk}, Measured

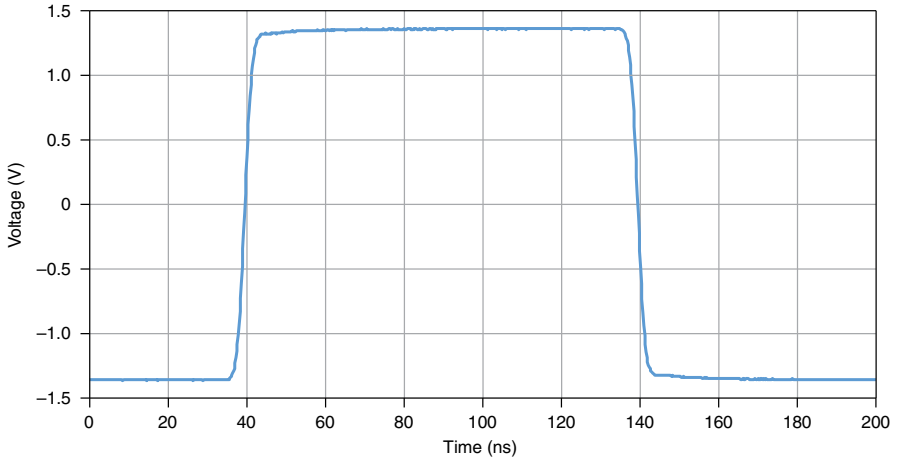
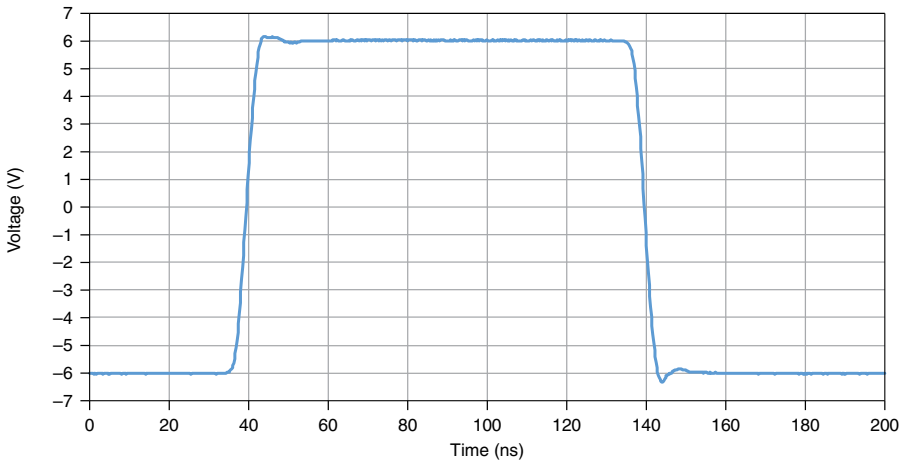


Figure 7. Square Waveform Step Response at 12 V_{pk-pk}, Measured



²³ Integrated from 10 Hz to 10 MHz using a 27 MHz square wave.

Ramp and Triangle Waveforms

Frequency range

2.75 V_{pk-pk} 0 MHz to 50 MHz

12 V_{pk-pk} 0 MHz to 30 MHz

Noise Function

Gaussian noise

Bandwidth 100 MHz, measured

Crest factor 5, measured

Repetition period 5,849 years

User-Defined Function

Frequency range 0 MHz to 80 MHz

Frequency step size 2.84 μ Hz

Waveform points 8,192

Step response rise time

2.75 V_{pk-pk} 2.4 ns, measured

12 V_{pk-pk} 2.7 ns, measured

Arbitrary Waveform

Waveform size 4 samples to 256,000,000 samples

User sample rate

Digital filter enabled 5.6 μ S/s to 400 MS/s

Digital filter disabled 10 S/s to 250 MS/s

Waveform filters

Digital filter enabled *Bandwidth = 0.2 * User Sample Rate*

Digital filter disabled No reconstruction image rejection

Minimum quantum size 1 sample

Rise time²⁴

Digital filter enabled 4.7 ns, measured

Digital filter disabled 3.4 ns, measured

Total onboard memory 512 MB per channel

²⁴ At maximum user sample rate.

Figure 8. Magnitude Response²⁵, Measured

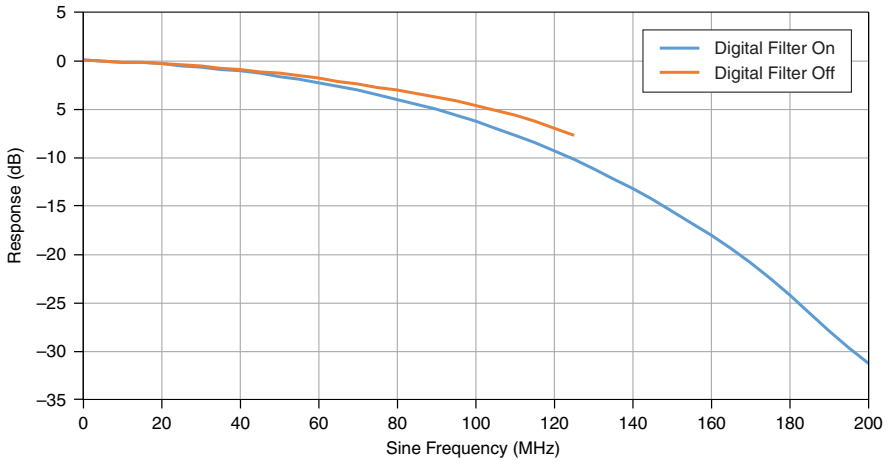
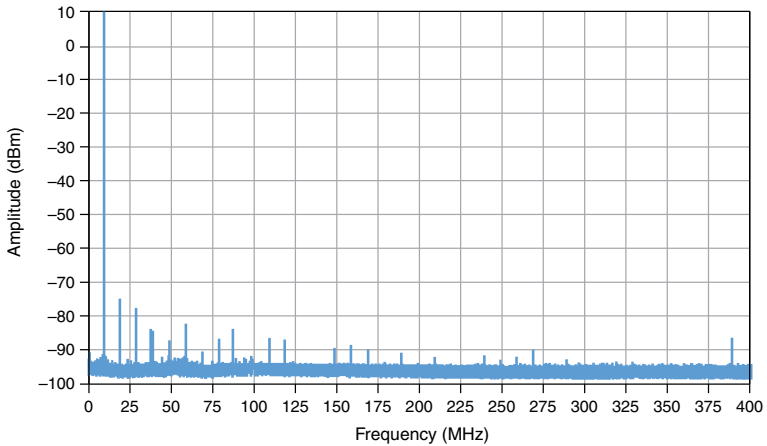


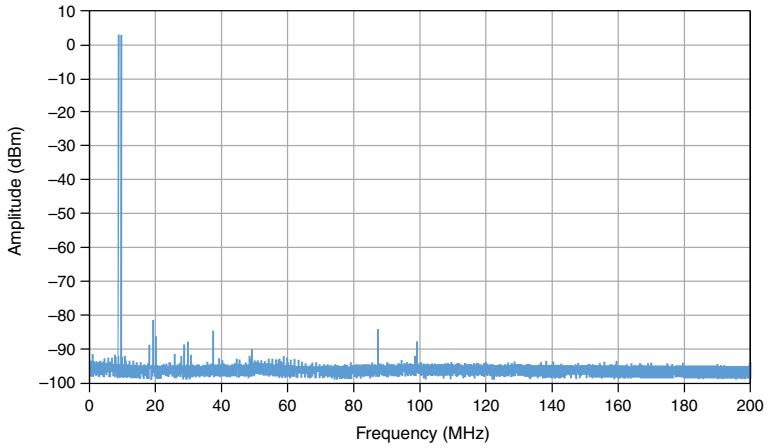
Figure 9. 10 MHz Single-Tone Spectrum²⁶, Measured



²⁵ Relative to 50 kHz and at 2 V_{pk-pk} and maximum user sample rate.

²⁶ With the digital filter enabled and at -1 dBFS, 2 V_{pk-pk}, and 400 MS/s. Noise floor is limited by the noise floor of the measurement device.

Figure 10. 9.5 MHz and 10.5 MHz Dual-Tone Spectrum²⁷, Measured



General

| | |
|--------------------|-----------------------|
| Total power | 26 W, typical |
| Bus interface | |
| Form factor | Gen 1 x4 module |
| Slot compatibility | PXI Express or hybrid |

²⁷ With the digital filter enabled and at -7 dBFS, 2 V_{pk-pk}, and 400 MS/s. Noise floor is limited by the noise floor of the measurement device.

Figure 11. Amplitude Versus Recommended Sine Wave Frequency

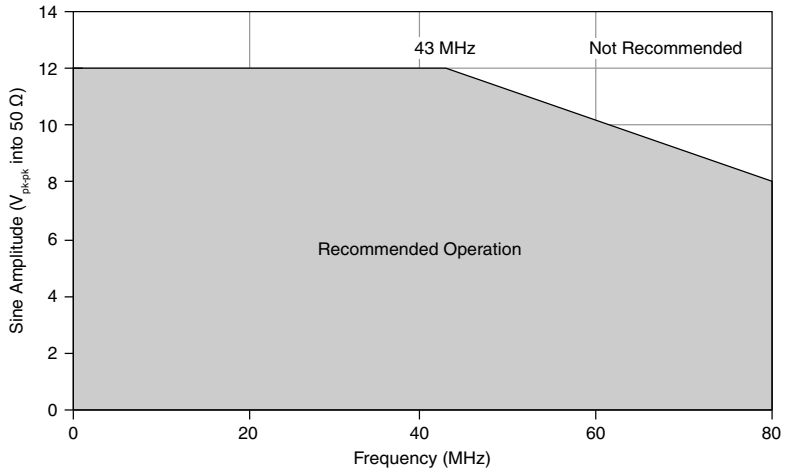


Figure 12. Channel-To-Channel Crosstalk, Measured

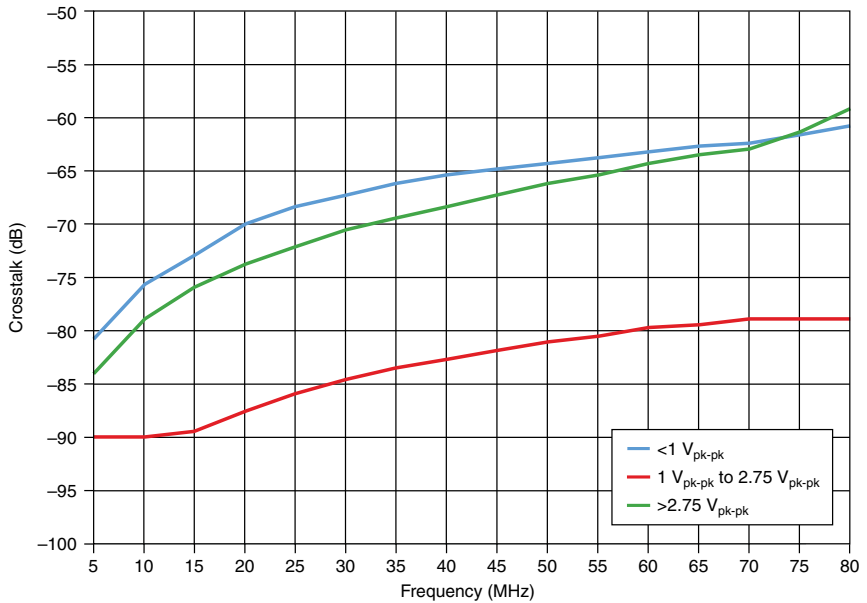
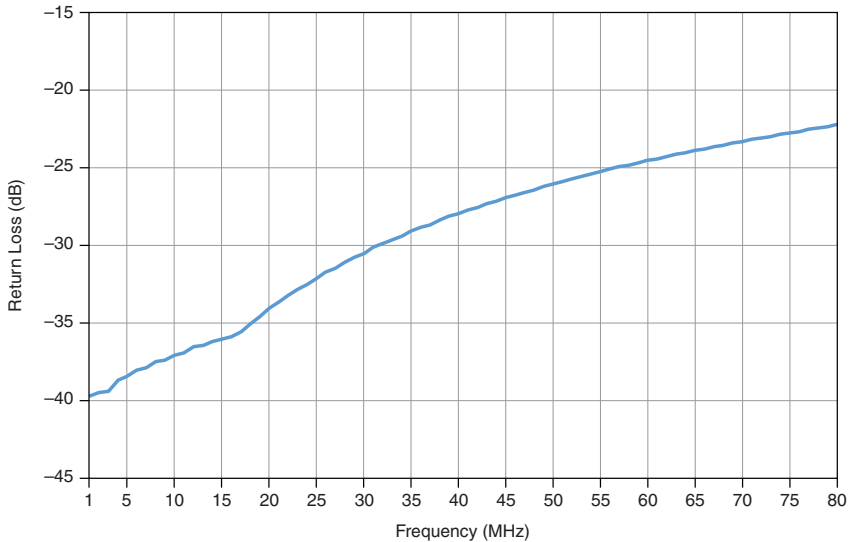


Figure 13. Return Loss, Measured



Clock

| | |
|--|--|
| Reference Clock source | Internal PXIe_CLK100 (backplane connector) |
| Reference Clock frequency | 100 MHz ($\leq \pm 25$ ppm) |
| Sample Clock rate | 800 MHz |
| Internal timebase accuracy ²⁸ | |
| Initial calibrated accuracy | 1.5 ppm, warranted |
| Time drift ²⁹ | 1 ppm per year, warranted |
| Accuracy | <i>Initial Calibrated Accuracy</i> \pm <i>Time Drift</i> , warranted |

²⁸ If locked to an external Reference Clock source, timebase accuracy is equal to the external Reference Clock accuracy.

²⁹ Where time drift starts at the latest external calibration date.

PFI I/O

| | |
|--------------------------|-------------------|
| Number of terminals | 10 |
| Connector type | |
| PFI 0 and PFI 1 | SMA |
| AUX 0/PFI <0..7> | MHDMR |
| Logic level | 3.3 V |
| Maximum input voltage | +5 V |
| V _{IH} | 2 V |
| V _{IL} | 0.8 V |
| Frequency range | 0 MHz to 25 MHz |
| PFI-to-channel crosstalk | -80 dBc, measured |

Trigger

| | |
|-----------------------------|--|
| Sources/destinations | PFI <0..1> (SMA front panel connectors) AUX 0/PFI <0..7> (MHDMR front panel connector) PXI_Trig <0..7> (backplane connector) |
| Supported triggers | Start Trigger Script Trigger |
| Trigger type | Rising edge |
| Trigger modes ³⁰ | Single Continuous Stepped Burst |
| Input impedance (DC) | >100 k Ω |

Marker

| | |
|--------------|--|
| Destinations | PFI <0..1> (SMA front panel connectors) AUX 0/PFI <0..7> (MHDMR front panel connector) PXI_Trig <0..6> (backplane connector) |
| Pulse width | >150 ns |

³⁰ In frequency list, arbitrary waveform, and arbitrary sequence output modes.

Marker to output skew

| | |
|---|--------|
| PFI <0..1> and AUX 0/PFI <0..7> | ±2 ns |
| PXI_Trig <0..6> | ±20 ns |
| Maximum number of marker outputs per waveform | 4 |

Calibration

| | |
|------------------------------------|---|
| Self-calibration | An onboard reference is used to calibrate the DC gain and offset. The self-calibration is initiated by the user through the software and takes approximately 2 minutes to complete. |
| External calibration ³¹ | External calibration calibrates the TCXO, voltage reference, and DC gain and offset. Appropriate constants are stored in nonvolatile memory. |
| Calibration interval | Specifications valid within 2 years of external calibration |
| Warm-up time ³² | 15 minutes |

Environment

| | |
|------------------|---|
| Maximum altitude | 2,000 m (800 mbar) (at 25 °C ambient temperature) |
| Pollution Degree | 2 |
| Indoor use only. | |

³¹ Also known as factory calibration.

³² Warm up begins after the chassis is powered and the PXIe-5433 is recognized by the host and configured using NI-FGEN. Self-calibration is recommended following the warm-up time.

Operating Environment

| | |
|---------------------------|--|
| Ambient temperature range | 0 °C to 55 °C (Tested in accordance with IEC 60068-2-1 and IEC 60068-2-2. Meets MIL-PRF-28800F Class 3 low temperature limit and MIL-PRF-28800F Class 2 high temperature limit.) |
| Relative humidity range | 10% to 90%, noncondensing (Tested in accordance with IEC 60068-2-56.) |

Storage Environment

| | |
|---------------------------|---|
| Ambient temperature range | -40 °C to 71 °C (Tested in accordance with IEC 60068-2-1 and IEC 60068-2-2. Meets MIL-PRF-28800F Class 3 limits.) |
| Relative humidity range | 5% to 95%, noncondensing (Tested in accordance with IEC 60068-2-56.) |

Shock and Vibration

| | |
|------------------|--|
| Operating shock | 30 g peak, half-sine, 11 ms pulse (Tested in accordance with IEC 60068-2-27. Meets MIL-PRF-28800F Class 2 limits.) |
| Random vibration | |
| Operating | 5 Hz to 500 Hz, 0.3 g _{rms} (Tested in accordance with IEC 60068-2-64.) |
| Nonoperating | 5 Hz to 500 Hz, 2.4 g _{rms} (Tested in accordance with IEC 60068-2-64. Test profile exceeds the requirements of MIL-PRF-28800F, Class 3.) |

Physical

| | |
|--------------|--|
| Dimensions | 21.6 cm × 2.0 cm × 13.0 cm (8.5 in. × 0.8 in. × 5.1 in.) 3 U, one slot, PXI Express module |
| Weight | |
| One channel | 369 g (13.0 oz) |
| Two channels | 376 g (13.3 oz) |

Compliance and Certifications

Safety

This product is designed to meet the requirements of the following electrical equipment safety standards for measurement, control, and laboratory use:

- IEC 61010-1, EN 61010-1
- UL 61010-1, CSA C22.2 No. 61010-1



Note For UL and other safety certifications, refer to the product label or the [Online Product Certification](#) section.

Electromagnetic Compatibility

This product meets the requirements of the following EMC standards for electrical equipment for measurement, control, and laboratory use:

- EN 61326-1 (IEC 61326-1): Class A emissions; Basic immunity
- EN 55011 (CISPR 11): Group 1, Class A emissions
- EN 55022 (CISPR 22): Class A emissions
- EN 55024 (CISPR 24): Immunity
- AS/NZS CISPR 11: Group 1, Class A emissions
- AS/NZS CISPR 22: Class A emissions
- FCC 47 CFR Part 15B: Class A emissions
- ICES-001: Class A emissions



Note In the United States (per FCC 47 CFR), Class A equipment is intended for use in commercial, light-industrial, and heavy-industrial locations. In Europe, Canada, Australia, and New Zealand (per CISPR 11), Class A equipment is intended for use only in heavy-industrial locations.



Note Group 1 equipment (per CISPR 11) is any industrial, scientific, or medical equipment that does not intentionally generate radio frequency energy for the treatment of material or inspection/analysis purposes.



Note For EMC declarations, certifications, and additional information, refer to the [Online Product Certification](#) section.

CE Compliance

This product meets the essential requirements of applicable European Directives, as follows:

- 2014/35/EU; Low-Voltage Directive (safety)
- 2014/30/EU; Electromagnetic Compatibility Directive (EMC)

Online Product Certification

Refer to the product Declaration of Conformity (DoC) for additional regulatory compliance information. To obtain product certifications and the DoC for this product, visit ni.com/certification, search by model number or product line, and click the appropriate link in the Certification column.

Environmental Management

NI is committed to designing and manufacturing products in an environmentally responsible manner. NI recognizes that eliminating certain hazardous substances from our products is beneficial to the environment and to NI customers.

For additional environmental information, refer to the *Minimize Our Environmental Impact* web page at ni.com/environment. This page contains the environmental regulations and directives with which NI complies, as well as other environmental information not included in this document.

Waste Electrical and Electronic Equipment (WEEE)



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