

**Board Assembly Part Number(s)**

Part Number	Description
187965C-01L or later	NI GPIB-USB-HS

**Manufacturer:** National Instruments

**Volatile Memory**

Type <sup>1</sup>	Size	User Accessible/ System Accessible <sup>2</sup>	Battery Backup?	Purpose	Method of Clearing <sup>3</sup>
ASIC <sup>4</sup>	undefined	Yes/Yes	No	GPIB Communication	Cycle power
USB Microcontroller	undefined	No/Yes	No	USB Communication	Cycle Power
SRAM	256kbit	No/Yes	No	Configuration	Cycle Power

**Non-Volatile Memory**

Type	Size	User Accessible/ System Accessible	Battery Backup?	Purpose	Method of Clearing
EEPROM	256kbit	No/Yes	No	Configuration	None

**Media Storage**

Type	Size	User Accessible/ System Accessible	Battery Backup?	Purpose	Method of Clearing
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NONE

<sup>1</sup> Calibration constants that are stored in device EEPROMs include information for the device's full operating range. Calibration constants do not maintain any unique data for specific configurations at which the device is used unless otherwise specified.

<sup>2</sup> Items are designated **No** for the following reason(s):

- a) Hardware changes or a unique software tool from National Instruments are required to modify contents of the memory listed.
- b) Hardware-modifying software tools are not distributed to customers for any personal access or customization, also known as non-normal use.

<sup>3</sup> The designation *None Available to User* indicates that the ability to clear this memory is not available to the user under normal operation. The utilities required to clear the memory are not distributed by National Instruments to customers for normal use.

<sup>4</sup> All GPIB hardware solutions include ASICs (chips) that perform the GPIB communication. Common ASICs on National Instruments boards include the TNT4882C, TNT5004, NAT7210, and NAT9914. These chips all have buffers to store bytes received or sent over GPIB as well as registers for communication to the computer's bus. These buffers are internal to the chips themselves. All of these memories are cleared when power is turned off.

**Terms and Definitions**

**User Accessible** Allows the user to directly write or modify the contents of the memory during normal instrument operation.

**System Accessible** Does not allow the user to access or modify the memory during normal instrument operation. However, system accessible memory may be accessed or modified by background processes. This can be something that is not deliberate by the user and can be a background driver implementation, such as storing application information in RAM to increase speed of use.

**Cycle Power** The process of completely removing power from the device and its components. This process includes a complete shutdown of the PC and/or chassis containing the device; a reboot is not sufficient for the completion of this process.

**Volatile Memory** Requires power to maintain the stored information. When power is removed from this memory, its contents are lost.

**Non-Volatile** Retains its contents when power is removed. This type of memory typically contains calibration or chip configuration information, such as power up states.