

SPECIFICATIONS

PXI-5922

24-Bit, Flexible Resolution PXI Oscilloscope

Contents

Definitions.....	2
Conditions.....	2
Vertical.....	2
Analog Input.....	2
Impedance and Coupling.....	2
Voltage Levels.....	3
Accuracy.....	3
Bandwidth and Transient Response.....	4
Spectral Characteristics.....	6
Skew, Input Bias Current.....	9
Settling Time.....	10
Horizontal.....	11
Sample Clock.....	11
Onboard Clock (Internal VCXO).....	11
Phase-Locked Loop (PLL) Reference Clock.....	12
Trigger.....	13
Reference (Stop) Trigger.....	13
External Trigger.....	14
PFI 0 and PFI 1 (Programmable Function Interface, AUX Front Panel Connectors)....	14
Waveform Specifications.....	15
Calibration.....	16
Software.....	16
Driver Software.....	16
Application Software.....	16
Interactive Soft Front Panel and Configuration.....	16
TClk Specifications.....	16
Power.....	17
Physical.....	17
Environment.....	18
Operating Environment.....	18
Storage Environment.....	18
Shock and Vibration.....	18
Compliance and Certifications.....	19
Safety.....	19
Electromagnetic Compatibility.....	19

CE Compliance	20
Online Product Certification.....	20
Environmental Management.....	20

Definitions

Warranted specifications describe the performance of a model under stated operating conditions and are covered by the model warranty.

The following characteristic specifications describe values that are relevant to the use of the model under stated operating conditions but are not covered by the model warranty.

- *Typical* specifications describe the performance met by a majority of models.
- *Nominal* specifications describe an attribute that is based on design, conformance testing, or supplemental testing.

Conditions

Specifications are valid under the following conditions unless otherwise noted.

- Full operating temperature range
- All impedance selections
- All sample rates
- Source impedance $\leq 50 \Omega$

Typical specifications are valid under the following conditions unless otherwise noted:

- Ambient temperatures of 15 °C to 35 °C

Vertical

Analog Input

Number of channels	Software-selectable: two simultaneously sampling, single-ended or unbalanced differential channels or one differential channel
Connector	BNC

Impedance and Coupling

Input impedance	Software-selectable: 50 Ω $\pm 2.0\%$ or 1 M Ω $\pm 2.0\%$ in parallel with a typical capacitance of 60 pF
Input coupling	AC, DC, GND

Voltage Levels

Full-scale (FS) input range

$\pm 1 \text{ V}$ ($2 V_{\text{pk-pk}}$)
 $\pm 5 \text{ V}$ ($10 V_{\text{pk-pk}}$)

Maximum input overload

50 Ω

$7 V_{\text{rms}}$ with $|\text{Peaks}| \leq 10 \text{ V}$

1 M Ω

$|\text{Peaks}| \leq 42 \text{ V}$

Accuracy

Table 1. PXI-5922 Resolution

Sample Rate	Resolution
50 kS/s	24 bits
500 kS/s	24 bits
1 MS/s	22 bits
5 MS/s	20 bits
10 MS/s	18 bits
15 MS/s	16 bits

DC accuracy¹

2 $V_{\text{pk-pk}}$ range

$\pm(500 \text{ ppm}$ (0.05%) of input + 50 μV)

10 $V_{\text{pk-pk}}$ range

$\pm(500 \text{ ppm}$ (0.05%) of input + 100 μV)

DC drift²

2 $V_{\text{pk-pk}}$ range

$\pm(20 \text{ ppm}$ of input + 5 μV per $^{\circ}\text{C}$)

10 $V_{\text{pk-pk}}$ range

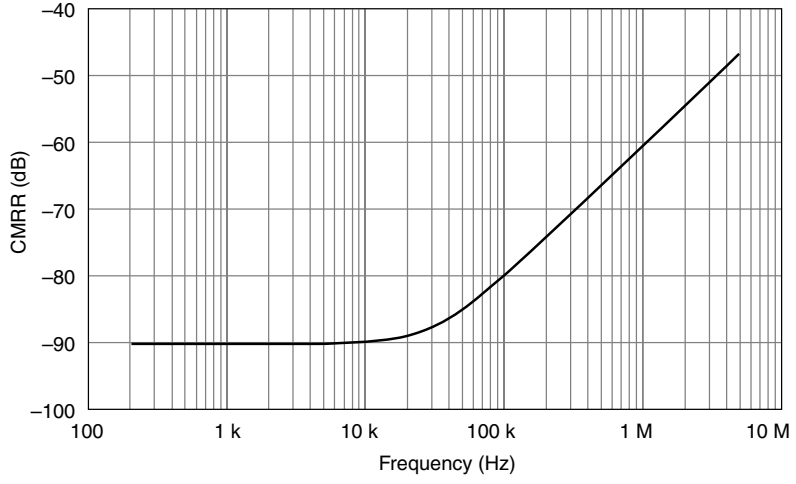
$\pm(20 \text{ ppm}$ of input + 10 μV per $^{\circ}\text{C}$)

¹ 1 M Ω input impedance; within $\pm 5 \text{ }^{\circ}\text{C}$ of self-calibration temperature; ppm = parts per million (1×10^{-6}).

² 1 M Ω input impedance.

AC amplitude accuracy	± 600 ppm (0.06%) at 1 kHz, typical ³
Crosstalk ⁴	
At 100 kHz	≤ -110 dB, typical
At 1 MHz	≤ -100 dB, typical
At 6 MHz	≤ -80 dB, typical
Common-mode rejection ratio (CMRR)	50 dB up to 1 kHz ⁵

Figure 1. PXI-5922 CMRR with Differential Terminal Configuration, Typical



Bandwidth and Transient Response

Alias-free bandwidth	$0.4 \times \text{Sample Rate}$
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Table 2. Alias Protection, Typical⁶

Sample Rate	Attenuation
<5 MS/s	100 dB
5 MS/s	96 dB

³ 1 M Ω input impedance; within ± 5 °C of self-calibration temperature.
⁴ CH 0 to/from CH 1, External Trigger to CH 0 or CH 1.
⁵ Unbalanced differential input terminal configuration.
⁶ Input frequencies $\geq 0.6 \times \text{Sample Rate}$.

Table 2. Alias Protection, Typical⁶ (Continued)

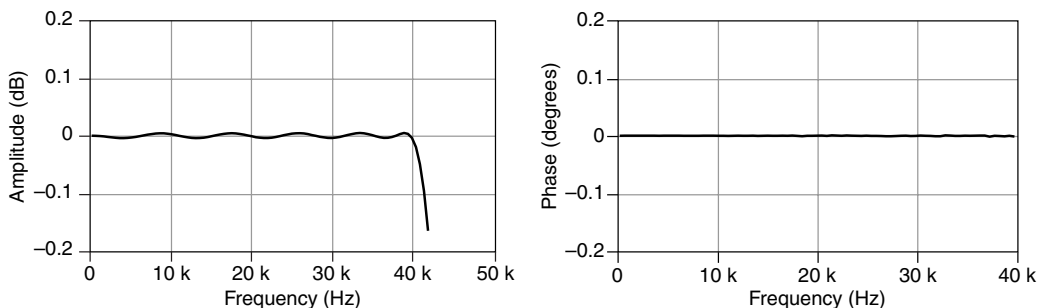
Sample Rate	Attenuation
$5 \text{ MS/s} < \text{rate} < 7.5 \text{ MS/s}$	90 dB
$7.5 \text{ MS/s} \leq \text{rate} \leq 15 \text{ MS/s}$	80 dB

AC coupling cutoff (-3 dB) 90 Hz

Table 3. Passband Flatness, Typical⁷

Sample Rate	50 Ω and 1 M Ω
1 MS/s	0.03 dB
5 MS/s	0.06 dB
10 MS/s	0.15 dB
15 MS/s	0.3 dB

Figure 2. 100 kS/s Frequency Response, Typical



⁶ Input frequencies $\geq 0.6 \times \text{Sample Rate}$.

⁷ Referenced to DC; input frequencies up to $0.4 \times \text{Sample Rate}$.

Figure 3. 1 MS/s Frequency Response, Typical

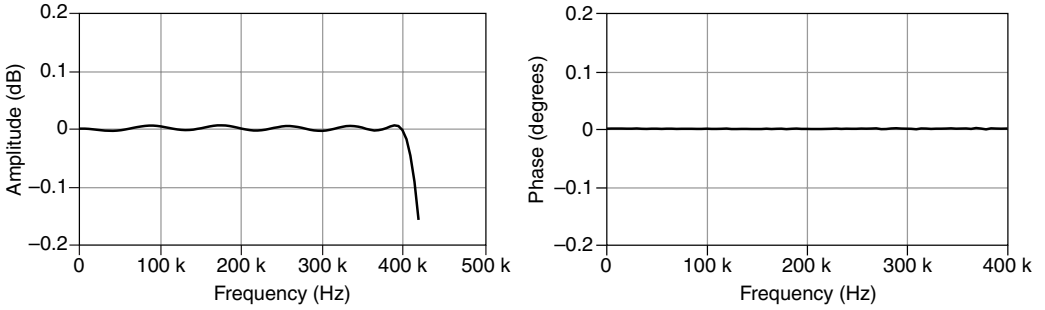
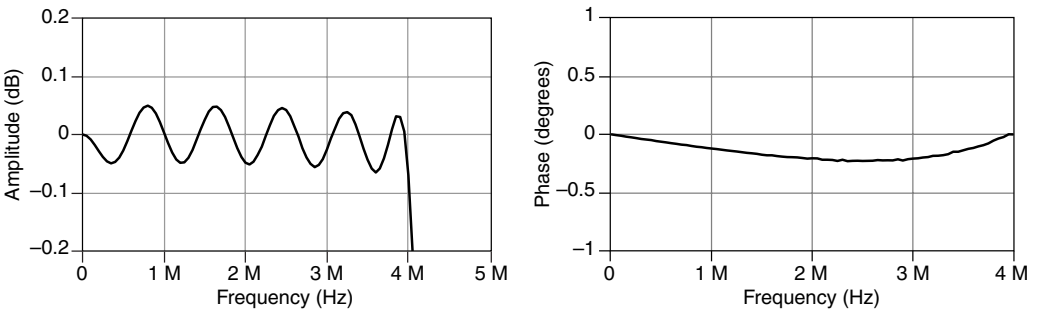


Figure 4. 10 MS/s Frequency Response, Typical



Spectral Characteristics

Table 4. Spurious-Free Dynamic Range (SFDR), Typical⁸

Input Frequency	Range	
	10 V _{pk-pk}	2 V _{pk-pk}
10 kHz	114 dBc	109 dBc
100 kHz	110 dBc	103 dBc
1 MHz	96 dBc	92 dBc

⁸ -1 dBFS input signal; *Sample Rate* is $10 \times$ input frequency; within ± 2 °C of self-calibration temperature.

Figure 5. PXI-5922 Dynamic Performance with 10 kHz Input Signal, Typical, 1 M Ω , 10 V_{pk-pk} Range, 500 kS/s, Unbalanced Differential, 10,000-Point FFT with 10 Averages

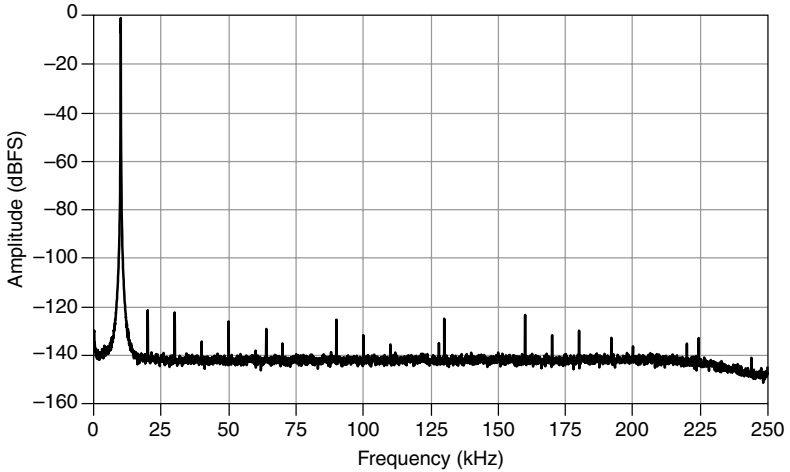


Figure 6. PXI-5922 Dynamic Performance with 10 kHz Input Signal, Typical, 1 M Ω , 2 V_{pk-pk} Range, 100 kS/s, Unbalanced Differential, 10,000-Point FFT with 10 Averages

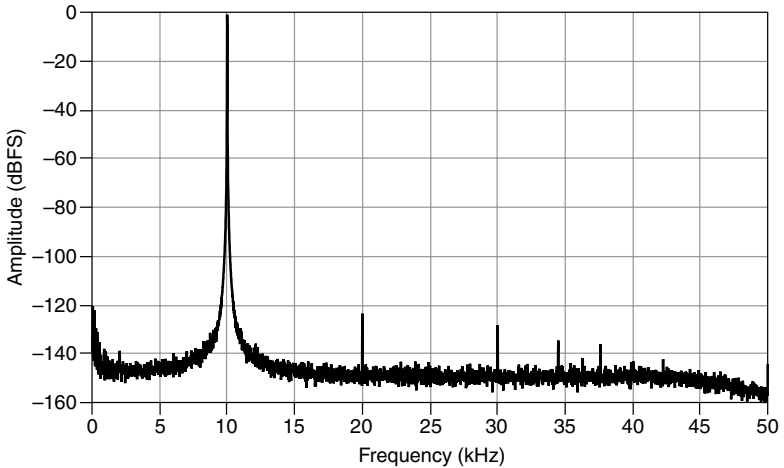


Table 5. Total Harmonic Distortion (THD), Typical⁹

Input Frequency	Range	
	10 V _{pk-pk}	2 V _{pk-pk}
10 kHz	-112 dBc	-107 dBc
100 kHz	-108 dBc	-101 dBc
1 MHz	-94 dBc	-90 dBc

Table 6. Signal-to-Noise and Distortion (SINAD), Typical¹⁰

Sample Rate	Range	
	10 V _{pk-pk}	2 V _{pk-pk}
1 MS/s	105 dB	99 dB
10 MS/s	89 dB	87 dB

Table 7. Signal-to-Noise Ratio (SNR) without Harmonics, Typical¹¹

Sample Rate	Range	
	10 V _{pk-pk}	2 V _{pk-pk}
1 MS/s	108 dB	104 dB
10 MS/s	91 dB	90 dB

⁹ -1 dBFS input signal; includes the second through the fifth harmonics; within ± 2 °C of self-calibration temperature .

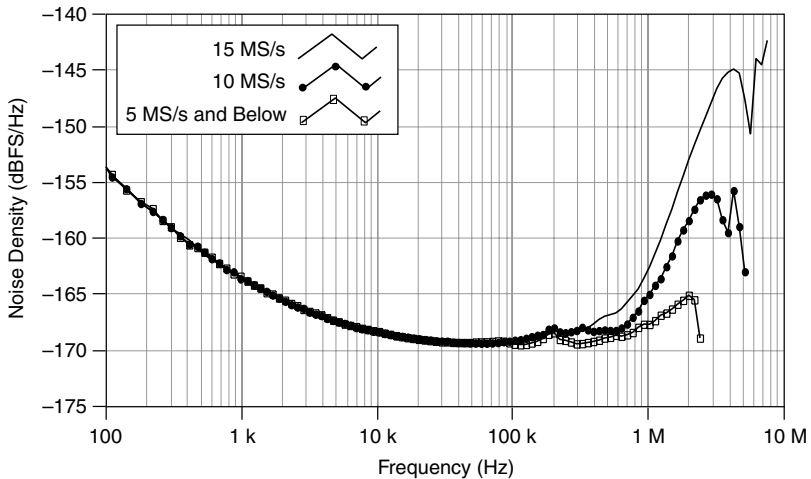
¹⁰ -1 dBFS input signal; input frequency is $0.1 \times \text{Sample rate}$; within ± 2 °C of self-calibration temperature; calculated from THD and RMS noise.

¹¹ -1 dBFS input signal; input frequency is $0.1 \times \text{Sample rate}$; within ± 2 °C of self-calibration temperature; calculated from SINAD and THD.

Table 8. RMS Noise¹²

Sample Rate	Range			
	10 V _{pk-pk}		2 V _{pk-pk}	
	dBFS	μV _{rms}	dBFS	μV _{rms}
50 kS/s	-120	3.4	-117	1.0
100 kS/s	-118	4.3	-115	1.2
1 MS/s	-108	13	-104	4.2
5 MS/s	-101	31	-98	8.7
10 MS/s	-91	92	-91	20
15 MS/s	-79	401	-79	80

Figure 7. PXI-5922 Noise Density, Typical



Skew, Input Bias Current

Channel-to-channel skew¹³ ≤500 ps, typical

Input bias current¹⁴ ≤500 nA

¹² 100 Hz to $0.4 \times \text{Sample rate}$; DC coupling; input 50 Ω terminated.

¹³ 1 MHz input, 5 MS/s sample rate.

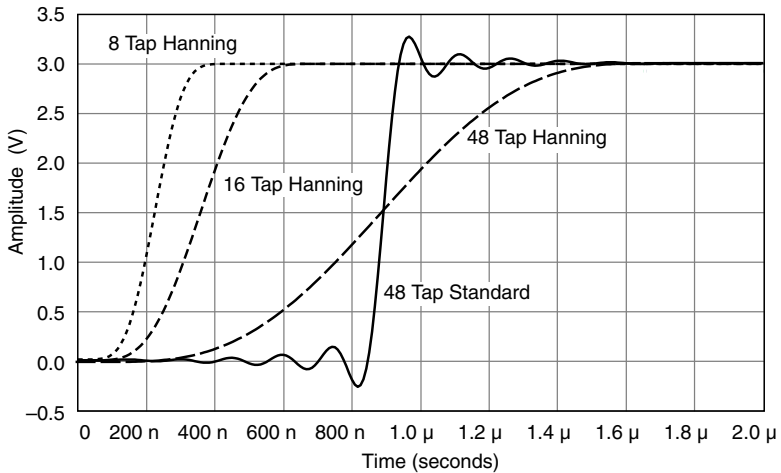
¹⁴ Within $\pm 5^\circ\text{C}$ of self-calibration temperature.

Settling Time

Table 9. Settling Time¹⁵

Filter Type ¹⁶	1%	0.01%
48 Tap Standard	800 ns	2.5 μ s
48 Tap Hanning	700 ns	1.5 μ s
16 Tap Hanning	300 ns	1.4 μ s
8 Tap Hanning	200 ns	1.3 μ s

Figure 8. PXI-5922 Step Response Using Different Filter Types¹⁷

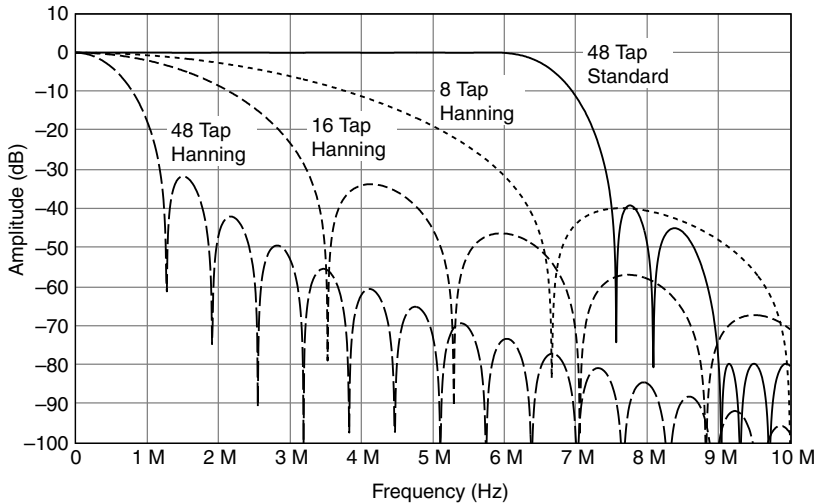


¹⁵ For a 3 V step from 0 V DC, excluding noise; time referenced to 1.5 V (50%) trigger; applies to 15 MS/s sample rate only.

¹⁶ To set or change the filter type, use the **Flex FIR Antialias Filter Type** property or the NISCOPE_ATTR_FLEX_FIR_ANTIALIAS_FILTER_TYPE attribute.

¹⁷ Time ($t=0$) represents the actual time the edge arrived at the BNC connector on the NI 5922.

Figure 9. PXI-5922 Frequency Response Using Different Filter Types



Horizontal

Sample Clock

Sources Internal onboard clock (internal VCXO)¹⁸

Onboard Clock (Internal VCXO)

Sample rate range, real-time sampling (single shot)¹⁹ 50 kS/s to 15 MS/s

Phase noise density (5 MHz input signal)

At 10 kHz <-133 dBc/Hz, typical

At 100 kHz <-145 dBc/Hz, typical

Sample clock jitter²⁰ ≤ 3 ps_{rms} (100 Hz to 1 MHz), typical

Timebase frequency 120 MHz

¹⁸ Internal Sample clock is locked to the Reference clock or derived from the onboard VCXO.

¹⁹ Available rates are $(60 \text{ MS/s})/n$ where n is an integer value from 4 to 1200. The Sample clock period is $n/(60\text{MS/s})$.

²⁰ Includes the effects of the converter aperture uncertainty and the clock circuitry jitter; excludes trigger jitter.

Timebase accuracy

Not phase-locked to Reference clock	±50 ppm, typical
Phase-locked to Reference clock	Equal to the Reference clock accuracy
Sample clock delay range	±1 Sample clock period
Sample clock delay resolution	400 ps

Phase-Locked Loop (PLL) Reference Clock

Reference clock sources	PXI_CLK 10 (backplane connector) CLK IN (front panel SMB connector)
Frequency range	1 MHz to 20 MHz in 1 MHz increments ²¹ ; must be accurate to ±50 ppm
Duty cycle tolerance	45% to 55%
Exported Reference clock destinations	CLK OUT (front panel SMB connector) PFI <0..1> (front panel 9-pin mini-circular DIN connector) PXI_TRIG <0..6> (backplane connector)

CLK IN (Reference Clock Input, Front Panel Connector)

Input voltage range	Square wave: $0.2 V_{pk-pk}$ to $1 V_{pk-pk}$
Maximum input overload	$7 V_{rms}$ with $ Peaks \leq 10 V$
Impedance	50 Ω
Coupling	AC

CLK OUT (Reference Clock Output, Front Panel Connector)

Output impedance	50 Ω
Logic type	5 V CMOS
Maximum drive current	±50 mA

²¹ The default value is 10 MHz.

Trigger

Reference (Stop) Trigger

Trigger types	Edge Window Hysteresis Digital Immediate Software
Trigger sources	CH 0 CH 1 TRIG PXI_Trig <0..6> PFI <0..1> PXI Star Trigger RTSI <0..6> Software
Time resolution	Sample clock period
Rearm time	$144 \times \text{Sample clock period}^{22}$
Holdoff	Up to $(2^{32} - 1) \times \text{Sample clock period}$

Related Information

Refer to the [NI High-Speed Digitizers Help](#) for more information about the sources available for each trigger type.

Analog Trigger

Trigger types	Edge Window Hysteresis
Sources ²³	CH 0 (front panel BNC connector) CH 1 (front panel BNC connector) TRIG (front panel BNC connector)
Trigger level range	100% FS

²² Holdoff set to 0.

²³ TRIG is an analog edge trigger only.

Edge trigger sensitivity	
CH 0, CH 1	2% FS, typical
TRIG (external trigger)	0.3 V _{pk-pk} up to 1 MHz, typical
Jitter	Sample clock period

Digital Trigger

Trigger type	Digital
Sources	PXI_TRIG <0..6> (backplane connector) PFI <0..1> (front panel 9-pin DIN connector) PFI Star Trigger (backplane connector)

External Trigger

Source	TRIG (front panel BNC connector)
Impedance	100 kΩ in parallel with 52 pF
Input voltage range	±2.5 V
Coupling	DC
Level accuracy	±0.3 V up to 100 kHz, typical
Maximum input overload	Peaks ≤42 V

PFI 0 and PFI 1 (Programmable Function Interface, AUX Front Panel Connectors)

Connector	9-pin mini-circular DIN
Direction	Bidirectional

As an Input (Trigger)

Destinations	Start trigger (acquisition arm) Reference (stop) trigger Arm Reference trigger Advance trigger
Input impedance	150 kΩ
V _{IH}	2.0 V
V _{IL}	0.8 V
Maximum input overload	-0.5 V, 5.5 V
Maximum frequency	25 MHz

As an Output (Event)

Sources	Start trigger (acquisition arm) Reference (stop) trigger End of Record Done (end of acquisition)
Output impedance	50 Ω
Logic type	3.3 V CMOS
Maximum drive current	± 24 mA
Maximum frequency	20 MHz

Waveform Specifications

Onboard memory size	
8 MB/channel	2 MS/channel
32 MB/channel	8 MS/channel
256 MB/channel	64 MS/channel
Minimum record length	1 Sample
Number of pretrigger samples	0 up to full Record Length for both single-record mode and multiple-record mode
Number of posttrigger samples	0 up to full Record Length for both single-record mode and multiple-record mode
Maximum number of records in onboard memory ²⁴	
8 MB/channel	13,107
32 MB/channel	52,428
256 MB/channel	100,000
Allocated onboard memory per record	$(Record\ Length \times 4\ bytes/S) + 400$ bytes, rounded up to next multiple of 128 bytes or 640 bytes, whichever is greater

²⁴ It is possible to exceed these numbers if you fetch records while acquiring data. For more information, refer to the *NI High-Speed Digitizers Help*.

Calibration

Self-calibration	Self-calibration is done on software command. The calibration corrects for gain and offset for all input ranges, input bias current, and nonlinearities in the ADCs.
External calibration (factory calibration)	The external calibration calibrates the VCXO and the voltage reference. Appropriate constants are stored in nonvolatile memory.
Interval for external calibration	2 years
Warm-up time	15 minutes

Software

Driver Software

Driver support for this device was first available in NI-SCOPE 2.8.

NI-SCOPE is an IVI-compliant driver that allows you to configure, control, and calibrate the PXI-5922. NI-SCOPE provides application programming interfaces for many development environments.

Application Software

NI-SCOPE provides programming interfaces, documentation, and examples for the following application development environments:

- LabVIEW
- LabWindows™/CVI™
- Measurement Studio
- Microsoft Visual C/C++
- .NET (C# and VB.NET)

Interactive Soft Front Panel and Configuration

The NI-SCOPE Soft Front Panel (SFP) allows interactive control of the PXI-5922.

Interactive control of the PXI-5922 was first available in NI-SCOPE SFP version 2.2. The NI-SCOPE SFP is included on the NI-SCOPE media.

NI Measurement Automation Explorer (MAX) also provides interactive configuration and test tools for the PXI-5922. MAX is included on the NI-SCOPE media.

TCIk Specifications

You can use the NI TCIk synchronization method and the NI-TCIk driver to align the Sample clocks on any number of supported devices, in one or more chassis. For more information

about TClk synchronization, refer to the *NI-TClk Synchronization Help*, which is located within the *NI High-Speed Digitizers Help*. For other configurations, including multichassis systems, contact NI Technical Support at ni.com/support.

Intermodule SMC Synchronization Using NI-TClk for Identical Modules

Specifications are valid under the following conditions:

- Any number of PXI modules installed in one NI PXI-1042 chassis.
- All parameters set to identical values for each SMC-based module.
- Sample clock set to 15 MS/s and all filters disabled.

Skew ²⁵	500 ps, typical
Average skew after manual adjustment	<10 ps, typical
Sample clock delay/adjustment resolution	≤5 ps, typical

Related Information

For information about manual adjustment, refer to the [Synchronization Repeatability Optimization topic in the NI-TClk Synchronization Help](#) within the [NI High-Speed Digitizers Help](#).

For additional help with the adjustment process, contact NI Technical support at ni.com/support.

Power

Current draw

+3.3 VDC	2.0 A, typical
+5 VDC	1.4 A, typical
+12 VDC	330 mA, typical
-12 VDC	280 mA, typical
Total power	20.9 W, typical

Physical

Dimensions	3U, one-slot, PXI/cPCI module 21.6 cm × 2.0 cm × 13.0 cm (8.5 in × 0.8 in × 5.1 in)
Weight	336 g (11.8 oz)

²⁵ Caused by clock and analog path delay differences. No manual adjustment performed.

Environment

Maximum altitude	2,000 m (at 25 °C ambient temperature)
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Pollution Degree	2
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Indoor use only.



Note To ensure that the PXI-5922 cools effectively, follow the guidelines in the *Maintain Forced-Air Cooling Note to Users* included in the kit or available at ni.com/manuals. The PXI-5922 is intended for indoor use only.

Operating Environment

Ambient temperature range	0 °C to 55 °C in all NI PXI chassis except the following: 0 °C to +45 °C when installed in an NI PXI-1000/B or PXI-101x chassis. (Tested in accordance with IEC 60068-2-1 and IEC 60068-2-2.)
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Relative humidity range	10% to 90%, noncondensing (Tested in accordance with IEC 60068-2-56.)
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Storage Environment

Ambient temperature range	-40 °C to 71 °C (Tested in accordance with IEC 60068-2-1 and IEC 60068-2-2.)
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Relative humidity range	5% to 95%, noncondensing (Tested in accordance with IEC 60068-2-56.)
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Shock and Vibration

Operational shock	30 g peak, half-sine, 11 ms pulse (Tested in accordance with IEC 60068-2-27. Test profile developed in accordance with MIL-PRF-28800F.)
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Storage Shock	50 g, half-sine, 11 ms pulse (Tested in accordance with IEC 60068-2-27. Test profile developed in accordance with MIL-PRF-28800F.)
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Random vibration

Operating	5 Hz to 500 Hz, 0.31 g _{rms} (Tested in accordance with IEC 60068-2-64.)
Nonoperating	5 Hz to 500 Hz, 2.46 g _{rms} (Tested in accordance with IEC 60068-2-64. Test profile exceeds the requirements of MIL-PRF-28800F, Class 3.)

Compliance and Certifications

Safety

This product is designed to meet the requirements of the following electrical equipment safety standards for measurement, control, and laboratory use:

- IEC 61010-1, EN 61010-1
- UL 61010-1, CSA C22.2 No. 61010-1



Note For UL and other safety certifications, refer to the product label or the [Online Product Certification](#) section.

Electromagnetic Compatibility

This product meets the requirements of the following EMC standards for electrical equipment for measurement, control, and laboratory use:

- EN 61326-1 (IEC 61326-1): Class A emissions; Basic immunity
- EN 55011 (CISPR 11): Group 1, Class A emissions
- EN 55022 (CISPR 22): Class A emissions
- EN 55024 (CISPR 24): Immunity
- AS/NZS CISPR 11: Group 1, Class A emissions
- AS/NZS CISPR 22: Class A emissions
- FCC 47 CFR Part 15B: Class A emissions
- ICES-001: Class A emissions



Note In the United States (per FCC 47 CFR), Class A equipment is intended for use in commercial, light-industrial, and heavy-industrial locations. In Europe, Canada, Australia, and New Zealand (per CISPR 11), Class A equipment is intended for use only in heavy-industrial locations.



Note Group 1 equipment (per CISPR 11) is any industrial, scientific, or medical equipment that does not intentionally generate radio frequency energy for the treatment of material or inspection/analysis purposes.



Note For EMC declarations, certifications, and additional information, refer to the [Online Product Certification](#) section.

CE Compliance

This product meets the essential requirements of applicable European Directives, as follows:

- 2014/35/EU; Low-Voltage Directive (safety)
- 2014/30/EU; Electromagnetic Compatibility Directive (EMC)

Online Product Certification

Refer to the product Declaration of Conformity (DoC) for additional regulatory compliance information. To obtain product certifications and the DoC for this product, visit [ni.com/certification](#), search by model number or product line, and click the appropriate link in the Certification column.

Environmental Management

NI is committed to designing and manufacturing products in an environmentally responsible manner. NI recognizes that eliminating certain hazardous substances from our products is beneficial to the environment and to NI customers.

For additional environmental information, refer to the *Minimize Our Environmental Impact* web page at [ni.com/environment](#). This page contains the environmental regulations and directives with which NI complies, as well as other environmental information not included in this document.

Waste Electrical and Electronic Equipment (WEEE)



EU Customers At the end of the product life cycle, all NI products must be disposed of according to local laws and regulations. For more information about how to recycle NI products in your region, visit [ni.com/environment/weee](#).

电子信息产品污染控制管理办法（中国 RoHS）



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